

Application/Control Number: 09/717,407

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Please delete from the present application, Claims 1-46 and Claims 52-56.

47. A single chip audio system comprising:
 a microcontroller for performing system control operations;
 a digital to analog converter for converting data selectively received from a bus interface and an accelerator interface;
 a mixer for mixing an analog signal output from said digital to analog converter with an analog signal received from a second source;
 circuitry for idling said mixer when selected signals input to said mixer are muted; and
 analog spatial enhancement circuitry for spatially enhancing analog audio data coupled from said mixer operable to:
 generate a difference signal from first and second signals output from said mixer;
 generate an acoustic crosstalk compensation signal from said difference signal;
 invert said compensation signal;
 sum said inverted compensation signal with said first signal; and
 sum said compensation signal with said second signal.
48. The audio system of Claim 47 wherein said accelerator interface is a serial interface.
49. The audio system of Claim 47 and further comprising a digital signal processor for selectively performing digital processing operations on digital data being input to said digital to analog converters.
50. The audio system of Claim 47 wherein said bus interface includes a PNP interface.

51. The audio system of Claim 49 wherein said digital signal processor performs SRS audio processing functions.